

Configuration Description



| Quantity | Type | Description |
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| 1 | HDRD | High Data Rate Demodulator, Differential ECL output, BPSK+QPSK+ 8 PSK capable, fully tunable up to 250Msym/s |
| 1 | CCSDS FEP | CCSDS Front-end Processor for Frame sync, Reed/Solomon (255, 239), (255, 223) and shortened decoding |
| 0 | DVB-S FEP | DVB-S Front-end Processor, Reed-Solomon (255, 238) decoding for 8PSK |
| 0 | HDRM | High Data Rate Modulator, Differential ECL input, BPSK+QPSK capable, fully Tunable up to 250MSym/s |
| 0 | HSO | High Speed Option, fully tunable up to 325Msym/s |
| 0 | XBDC | X-band Down Convertor, converts 7.9GHz - 8.4GHz to 750MHz input of the Omnisat-G3 |